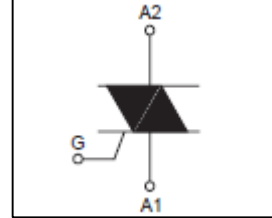


*Bi-Directional Triode Thyristor*

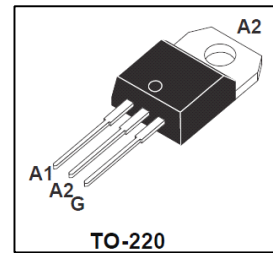
**Features**

- ◆ Repetitive Peak Off-State Voltage : 600V
- ◆ R.M.S On-State Current (  $I_T(\text{RMS})= 4 \text{ A}$  )
- ◆ Low On-State Voltage (1.6V(Typ.) @  $I_{\text{T(M)}}$ )
- ◆ High Commutation  $dv/dt$



**General Description**

Standard gate triggering Triac is suitable for direct coupling to TTL, HTL, CMOS and application such as various logic functions, low power AC switching applications, such as fan speed, small light controllers and home appliance equipment.



**Absolute Maximum Ratings ( $T_J= 25^\circ\text{C}$  unless otherwise specified)**

Symbol	Parameter	Condition	Ratings	Units	
$V_{\text{DRM}}/V_{\text{RRM}}$	Repetitive Peak Off-State Voltage		600	V	
$I_{\text{T(RMS)}}$	R.M.S On-State Current	$T_J = 110^\circ\text{C}$	4.0	A	
$I_{\text{TSM}}$	Surge On-State Current	One cycle, Peak value, non-repetitive full cycle	50Hz	30	A
			60Hz	31	
$I^2t$	$i^2t$		5.1	$\text{A}^2\text{s}$	
$P_{\text{GM}}$	Peak Gate Power Dissipation		5	W	
$P_{\text{G(AV)}}$	Average Gate Power Dissipation	$T_J = 125^\circ\text{C}$	1	W	
$I_{\text{GM}}$	Peak Gate Current	$T_J = 125^\circ\text{C}$	4.0	A	
$V_{\text{GM}}$	Peak Gate Voltage		7.0	V	
$T_J$	Operating Junction Temperature		-40~+150	$^\circ\text{C}$	
$T_{\text{STG}}$	Storage Temperature		-40~+150	$^\circ\text{C}$	

**Thermal Characteristics**

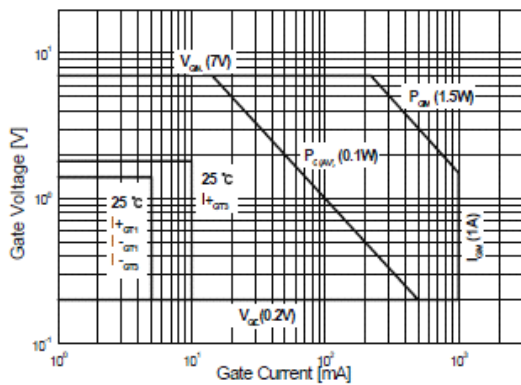
Symbol	Parameter	Value	Units
$R_{\theta\text{JC}}$	Thermal Resistance Junction to Case(DC)	2.6	$^\circ\text{C}/\text{W}$
$R_{\theta\text{JA}}$	Thermal Resistance Junction to Ambient(DC)	60	$^\circ\text{C}/\text{W}$

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

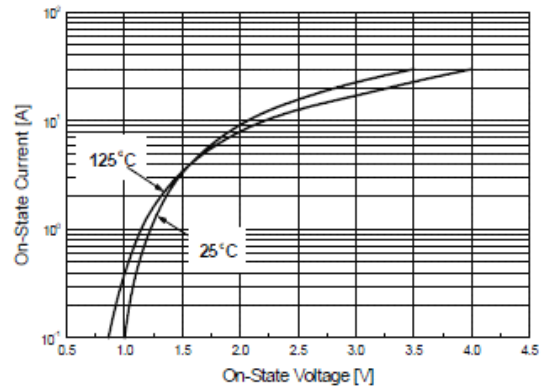
Symbol	Characteristics	Min	Typ.	Max	Unit	
I <sub>DRM</sub> /I <sub>RRM</sub>	off-state leakage current (V <sub>AK</sub> = V <sub>DRM</sub> /V <sub>RRM</sub> Single phase, half wave)	T <sub>J</sub> =25°C	-	-	5	μA
		T <sub>J</sub> =125°C	-	-	1	mA
V <sub>TM</sub>	Forward "On" voltage (I <sub>T</sub> =5A, Inst. Measurement)	-	1.2	1.6	V	
I <sub>GT</sub>	Gate trigger current (continuous dc) (V <sub>AK</sub> = 6 Vdc, R <sub>L</sub> = 10 Ω) Note:1	T2+,G+	-	-	10	mA
		T2+,G-	-	-	10	
		T2-,G-	-	-	10	
V <sub>GT</sub>	Gate Trigger Voltage (Continuous dc) (V <sub>AK</sub> = 6 Vdc, R <sub>L</sub> = 10 Ω) Note:1	T2+,G+	-	-	1.5	V
		T2+,G-	-	-	1.5	
		T2-,G-	-	-	1.5	
V <sub>GD</sub>	Gate threshold Voltage V <sub>D</sub> = 1/2V <sub>DRM</sub> , R <sub>L</sub> = 3.3K Ω	T <sub>J</sub> =125°C	0.2	-	-	V
dv/dt	Critical Rate of Rise of Off-State Voltage at Commutation (V <sub>D</sub> =0.67V <sub>DRM</sub> ;gate open) Note:2	T <sub>J</sub> =125°C	40	-	-	V/μs
I <sub>H</sub>	Holding Current	-	-	15	mA	
I <sub>L</sub>	latching current	-	-	30	mA	

Note 1: minimum IGT is guaranteed at 5% of IGT max.  
 2: for both polarities of A2 referenced to A1.

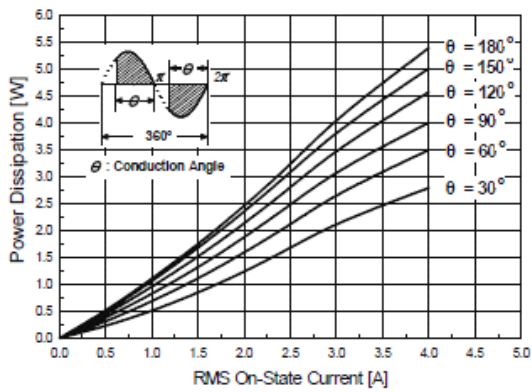
**Fig 1. Gate Characteristics**



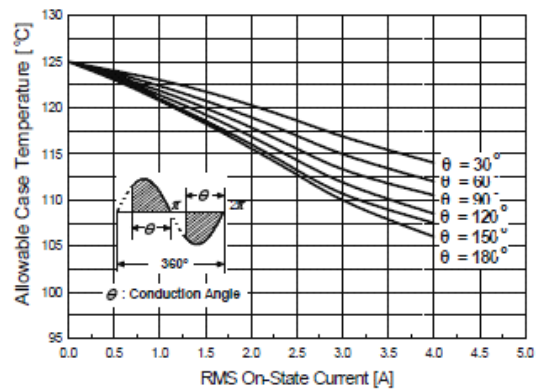
**Fig 2. On-State Voltage**



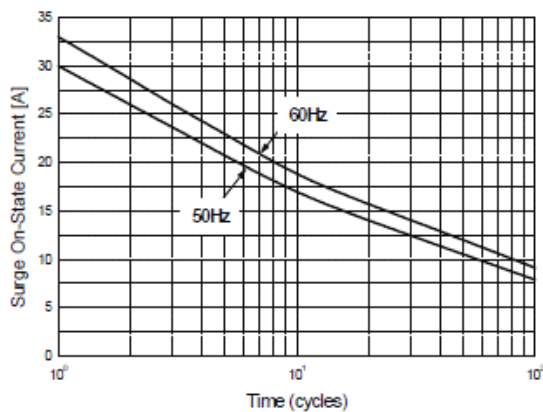
**Fig 3. On State Current vs. Maximum Power Dissipation**



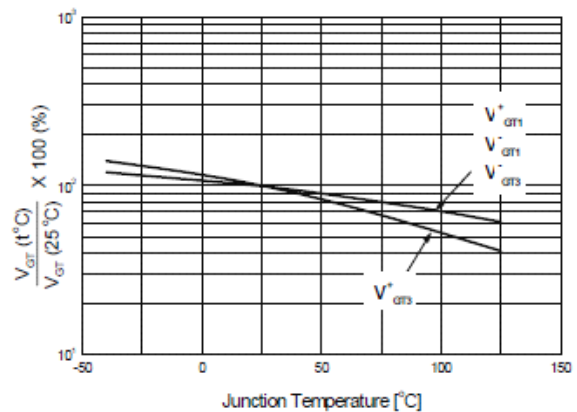
**Fig 4. On State Current vs. Allowable Case Temperature**



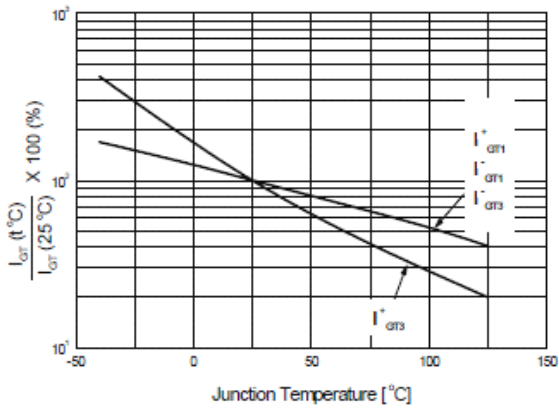
**Fig 5. Surge On-State Current Rating (Non-Repetitive)**



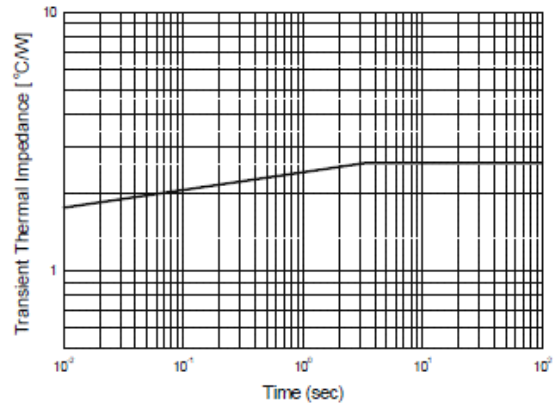
**Fig 6. Gate Trigger Voltage vs. Junction Temperature**



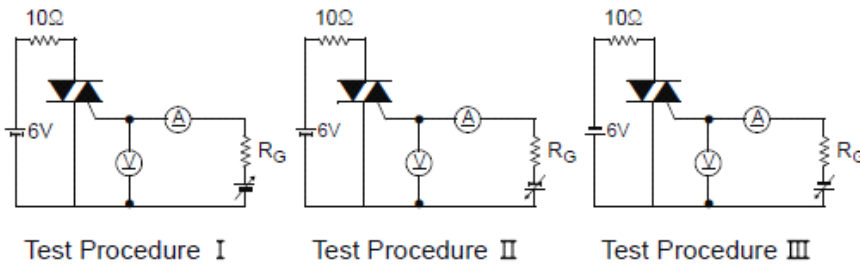
**Fig 7. Gate Trigger Current vs. Junction Temperature**



**Fig 8. Transient Thermal Impedance**



**Fig 9. Gate Trigger Characteristics Test Circuit**



**TO220 Package Dimension**

